

**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

Inventor: Warren M. Farnworth

Docket No.: 4212.3US

1/9

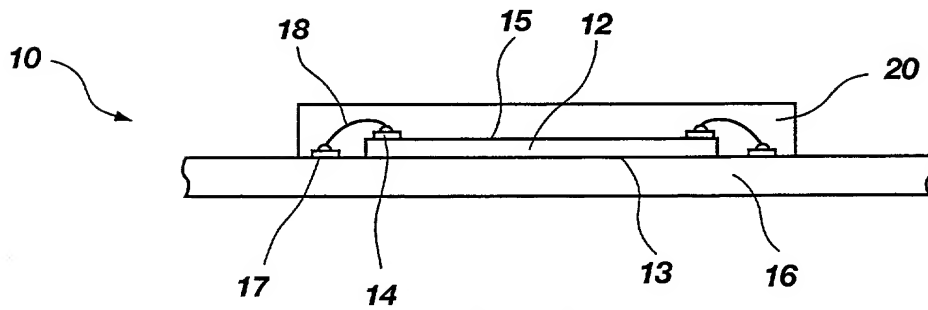


Fig. 1

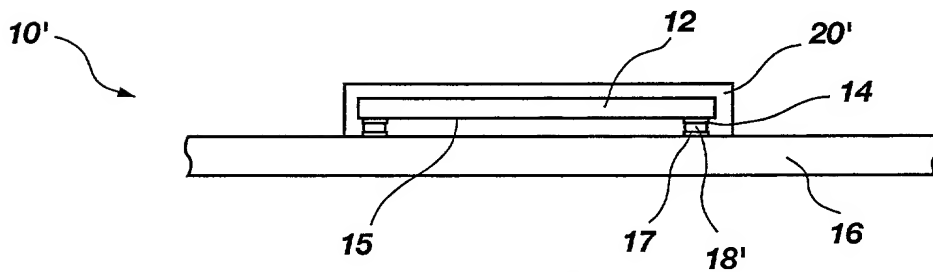


Fig. 2

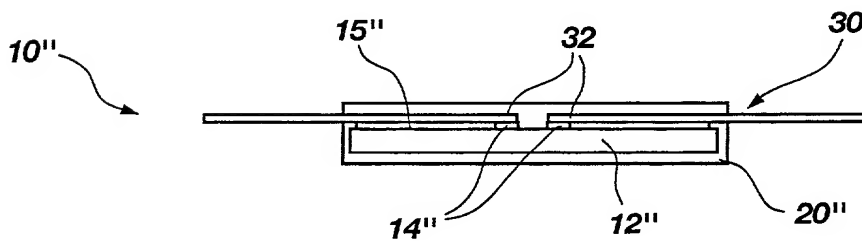


Fig. 3

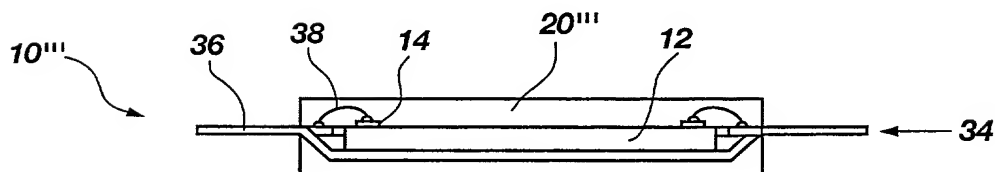


Fig. 4

**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

Inventor: Warren M. Farnworth

Docket No.: 4212.3US

2/9

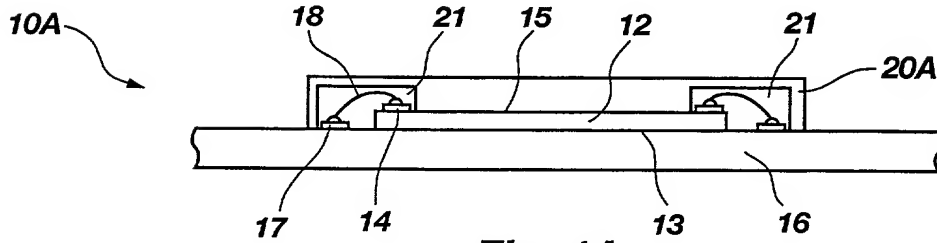


Fig. 1A

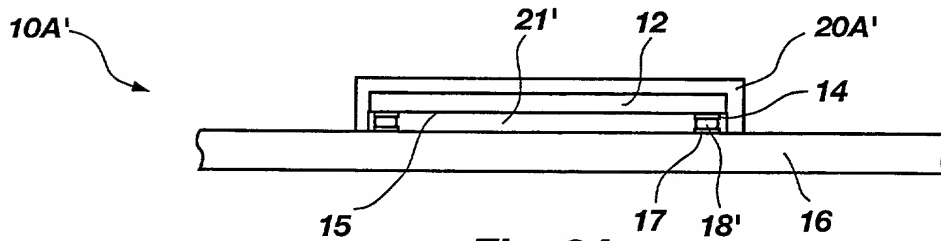


Fig. 2A

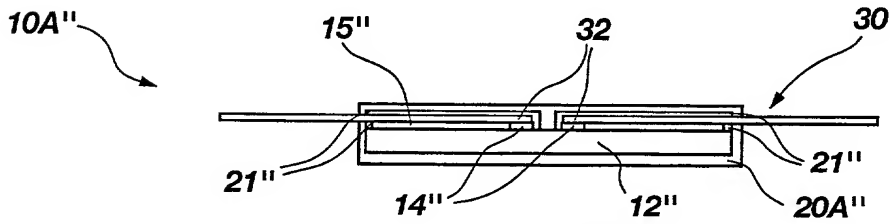


Fig. 3A

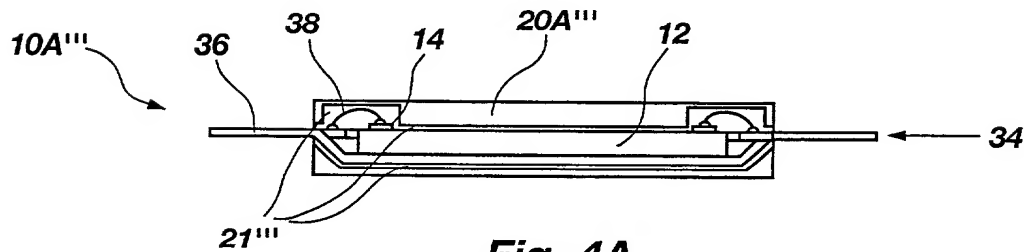


Fig. 4A

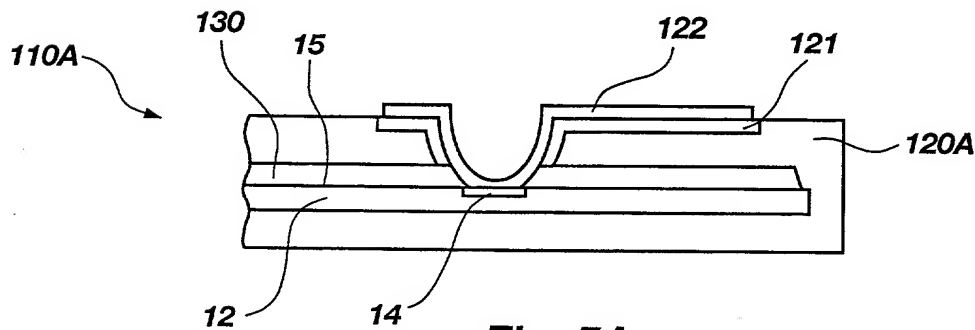


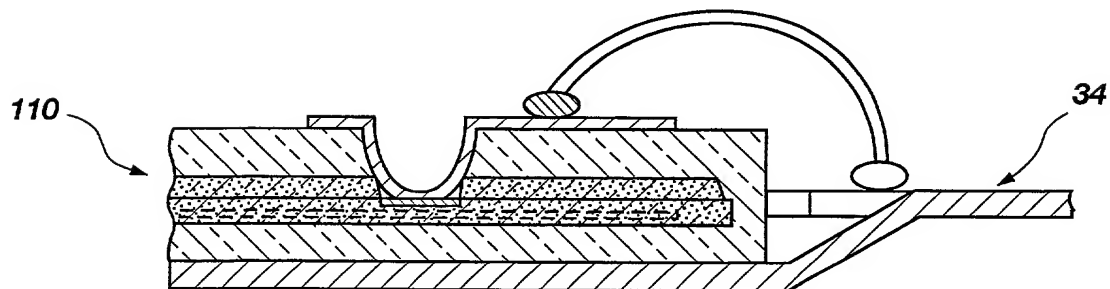
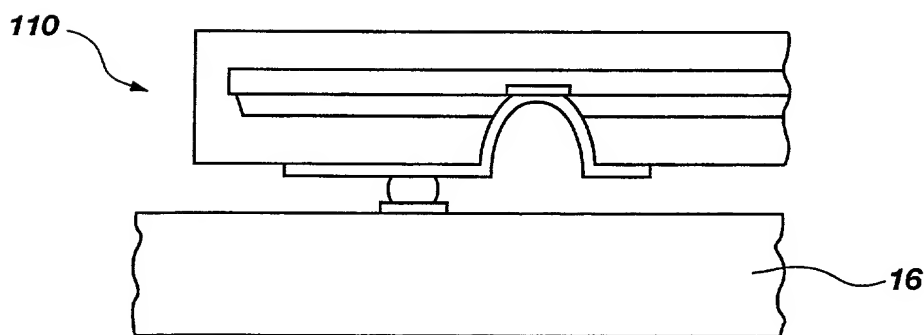
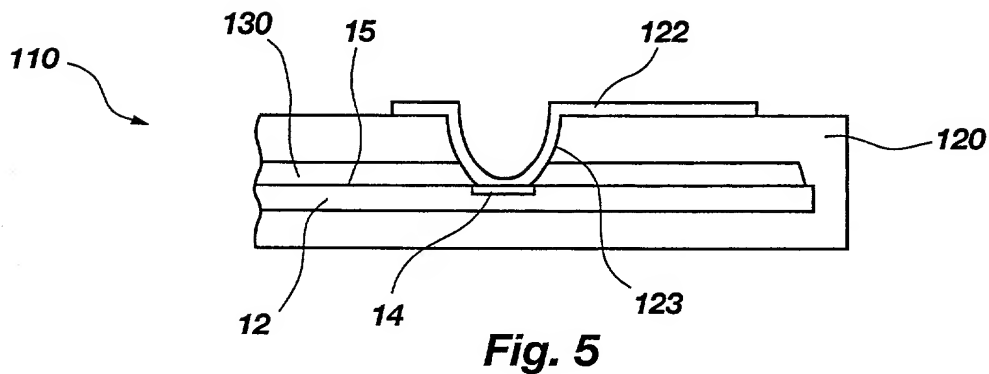
Fig. 5A

**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

Inventor: Warren M. Farnworth

Docket No.: 4212.3US

3/9



**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

Inventor: Warren M. Farnworth

Docket No.: 4212.3US

4/9

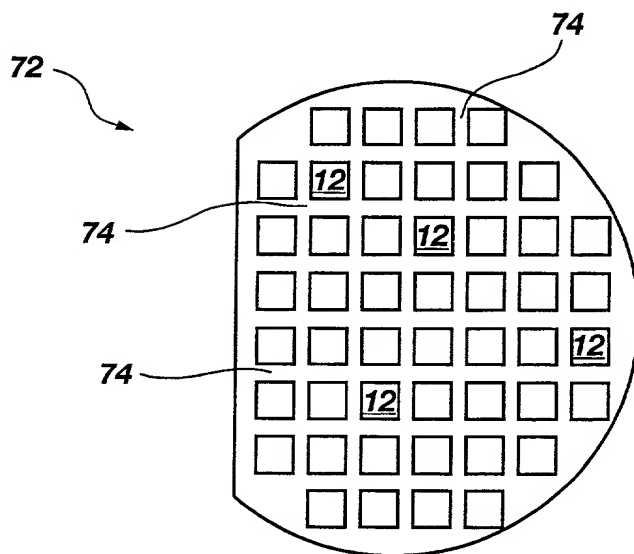


Fig. 8

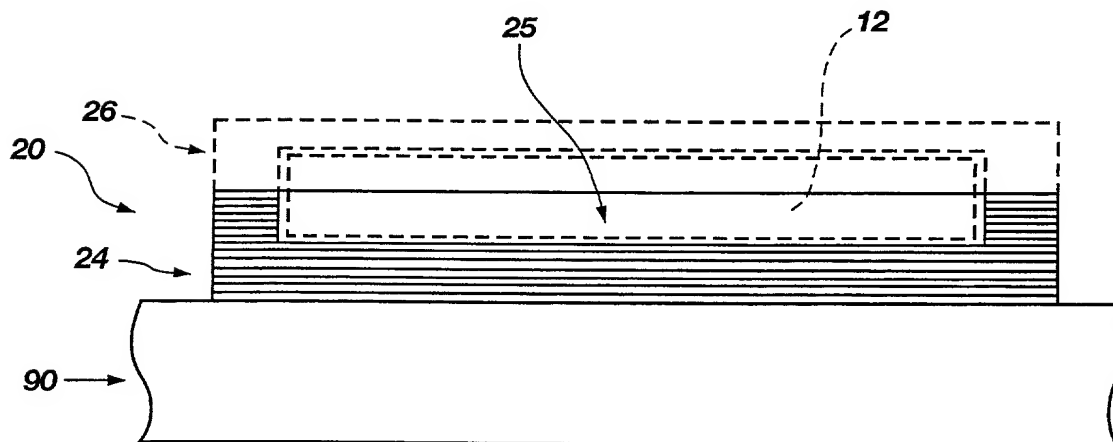
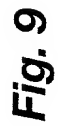


Fig. 18

[illegible]

5/9



**TITLE: STEREOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

**Inventor: Warren M. Farnworth
Docket No.: 4212.3US**

6/9

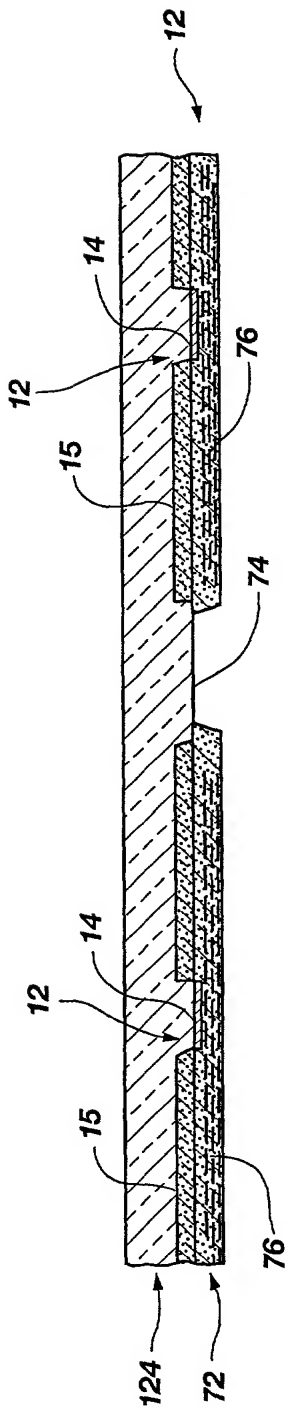


Fig. 11

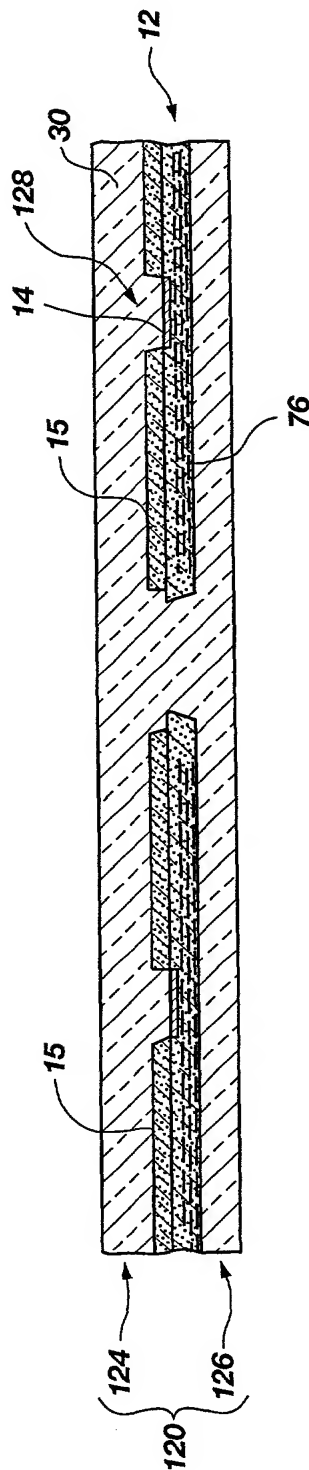


Fig. 12

TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES

Inventor: Warren M. Farnworth
Docket No.: 4212.3US

7/9

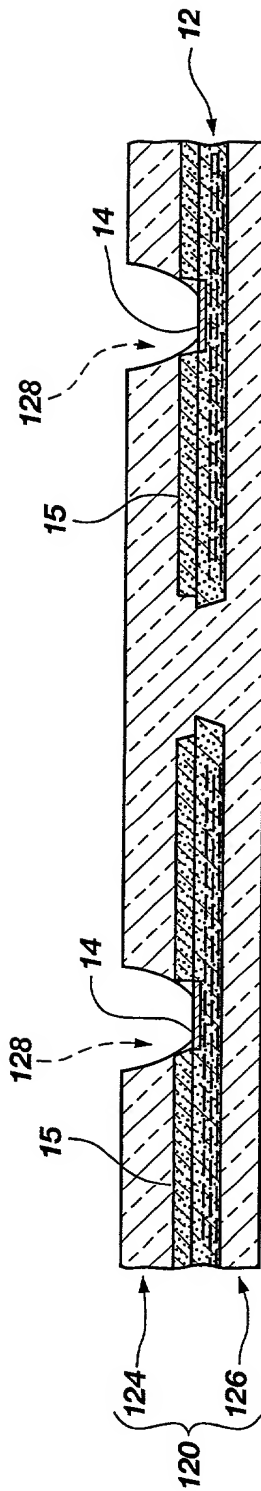


Fig. 13

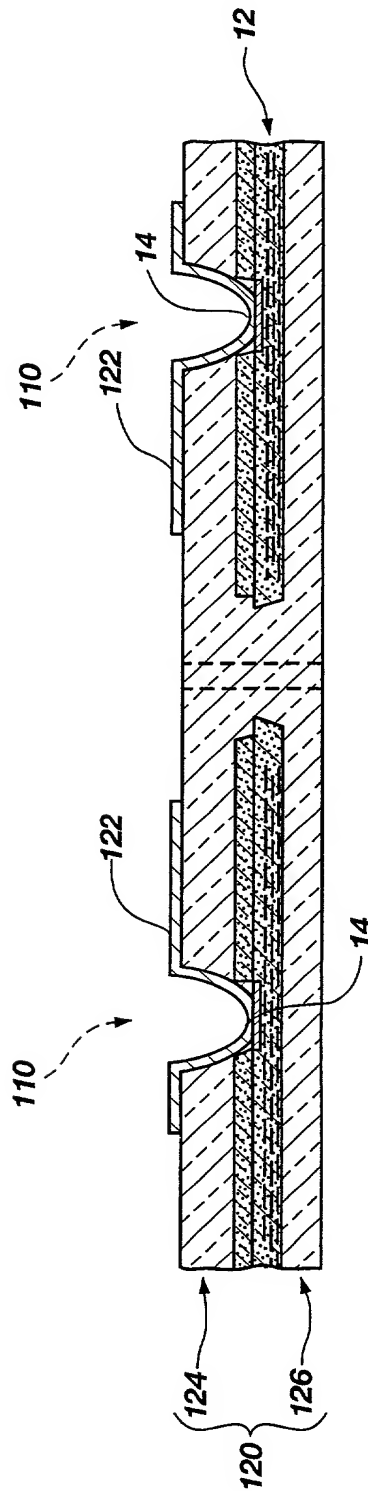


Fig. 14

**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

Inventor: Warren M. Farnworth

Docket No.: 4212.3US

8/9

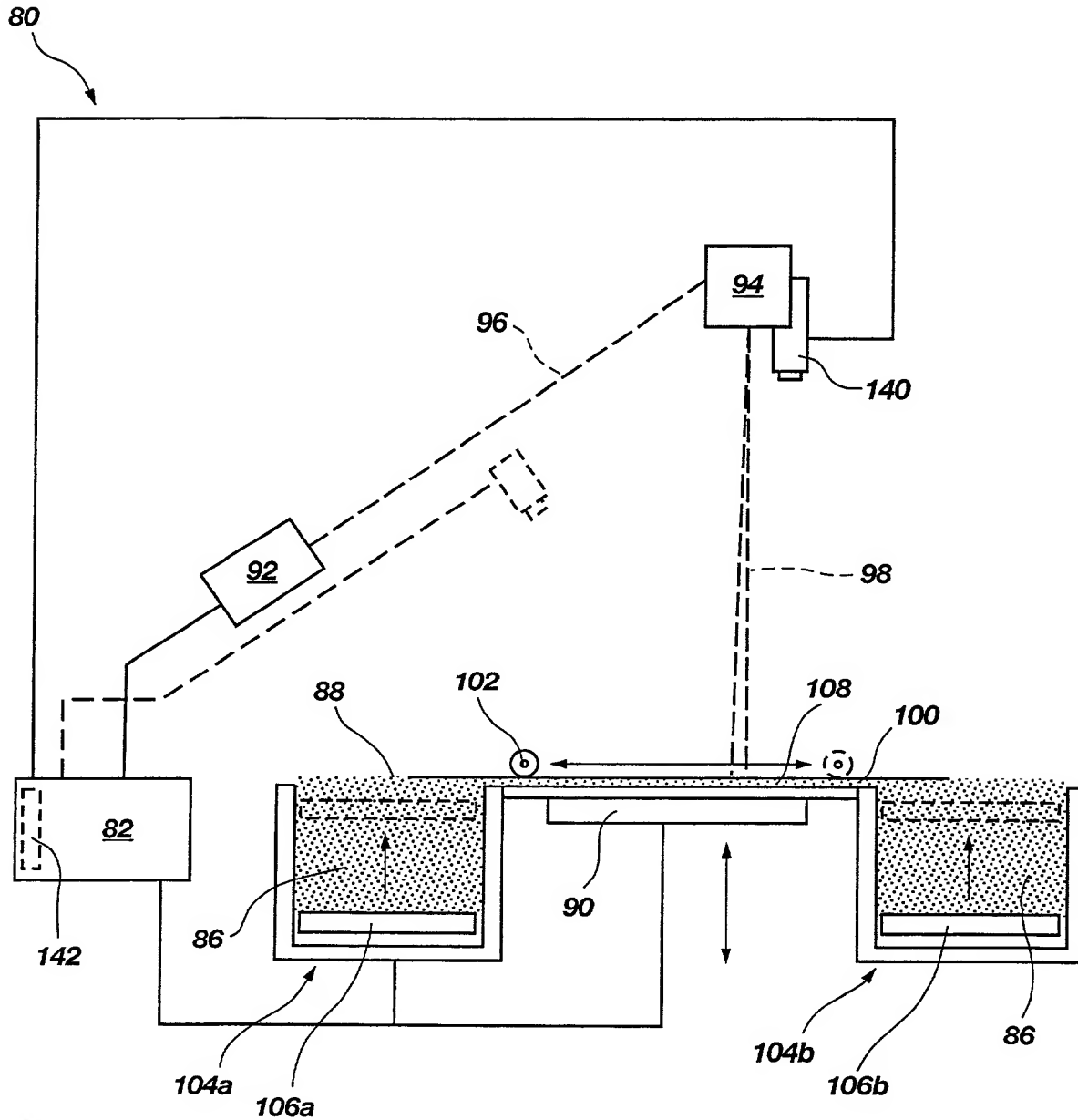


Fig. 15

**TITLE: STEREOLITHOGRAPHIC METHODS FOR
FABRICATING HERMETIC SEMICONDUCTOR DEVICE
PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC
PACKAGES**

**Inventor: Warren M. Farnworth
Docket No.: 4212.3US**

9/9

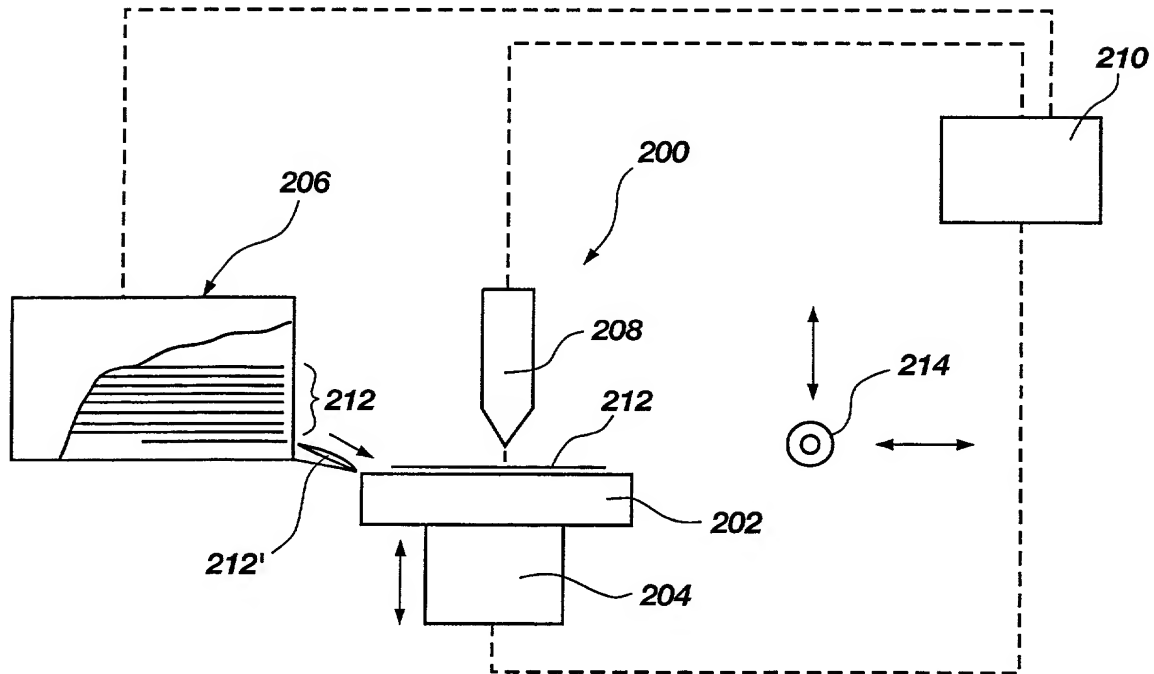


Fig. 16

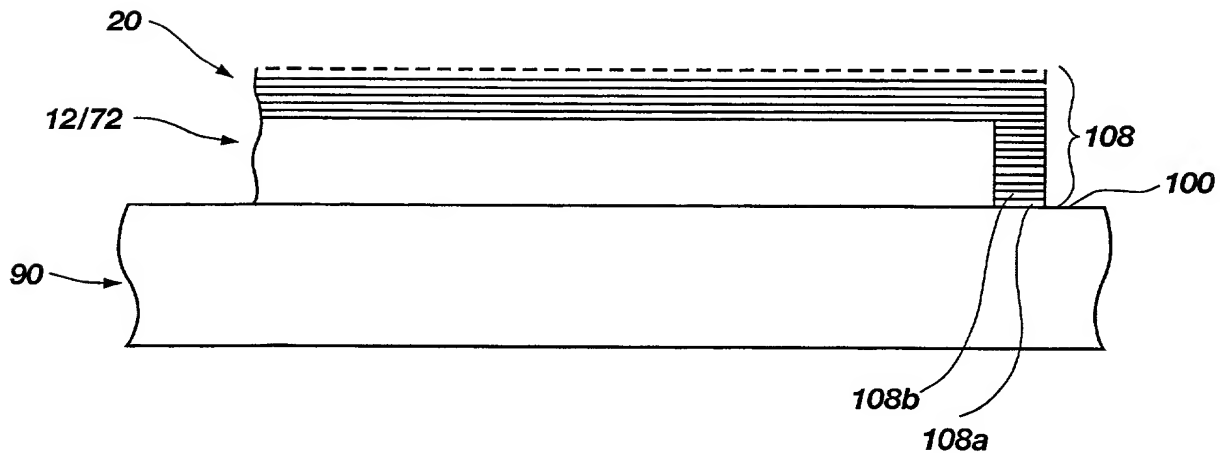


Fig. 17